

## REGISTERS

Most registers are formed using D-Type flip-flops. There are various configurations of registers. They can be serial in/out and parallel in/out.

These are SIPO, which clocks data in from a serial input and stores the data on the parallel outputs Q3-0

The PISO loads data in through parallel inputs D0-3 when the parallel-load signal is high. Clock pulses at the clock-in then serially clock the data out a bit at a time.

