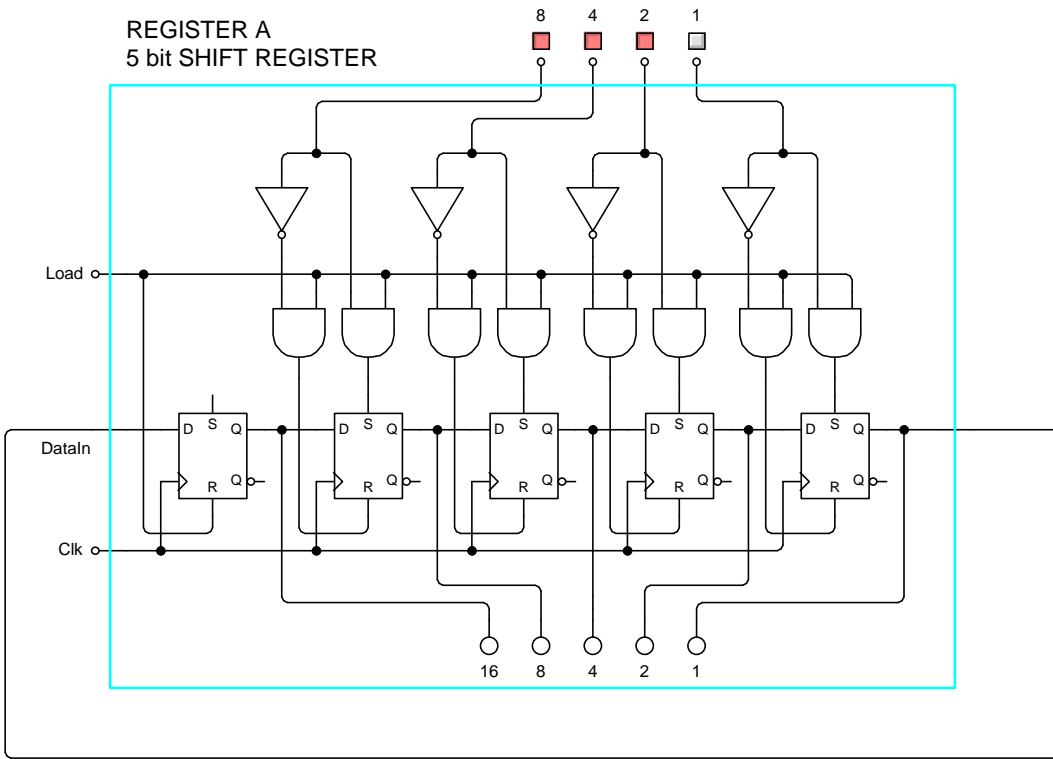
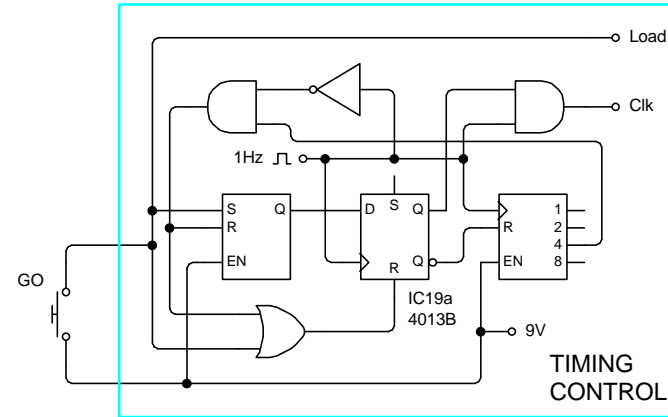


REGISTER A
5 bit SHIFT REGISTER



SERIAL 4 BIT SUBTRACTION

Set the inputs to Registers A and B then press GO.
The bits are shifted from the registers into the FULL ADDER. However Register B has its output **inverted** to provide 2's complement addition, effectively adding a negative number. The bits are added and the result fed back to Register A. Any CARRY is held in Register C to be added to the next significant bit. Note the carry register is set when commencing subtraction.



REGISTER B
4 bit SHIFT REG

