

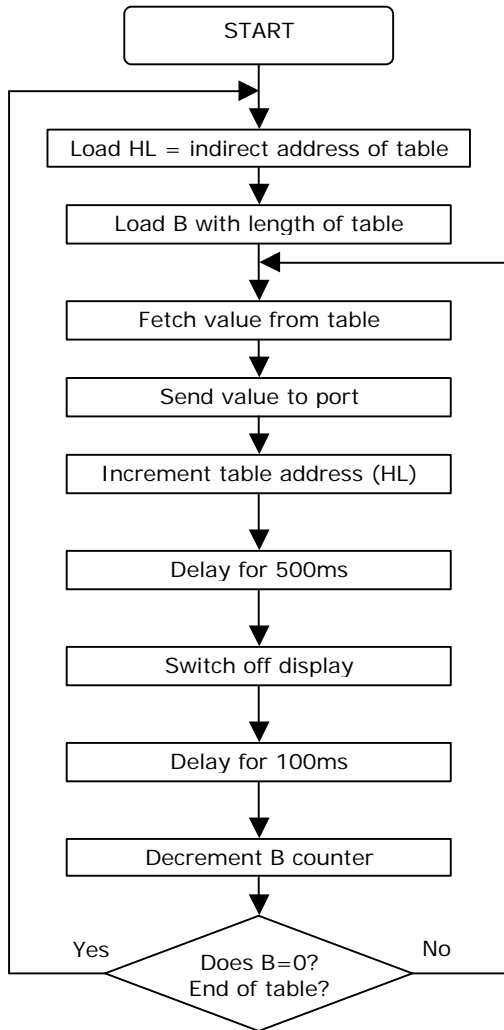
EZ-CPU CONTROL SYSTEM

DRIVING DISPLAYS - 1

PROGRAM:	ALPHA-NUMERIC DISPLAY	IP MODULE:	-
DESCRIPTION:	Displays a series of segment codes read from a table in intervals of 0.5s.	OP MODULE:	QSDD
		CPU SPEED:	1 KHz

Using indirect addressing, a list or table of segment codes can be sent to the output port. A delay to blank the display has been added to emphasise each character.

This is almost exactly the same routine as for the light sequencer. Can you explain why?



ADDR	INSTRUCTION	CODE
00	LD HL,0x0080	21 80 00
03	LD B,5	06 05
05	LD A,(HL)	7E
06	OUT (255),A	D3 FF
08	INC HL	23
09	LD A,125	3E 7D
0B	DEC A	3D
0C	JR NZ -3	20 FD
0E	LD A,0	3E 00
10	OUT (255),A	D3 FF
12	LD A,25	3E 19
14	DEC A	3D
15	JR NZ -3	20 FD
17	DEC B	05
18	JR NZ -12	20 EB
1A	JR -19	18 E4

DATA TABLE							
Table length = 5 bytes							
ADDR	DATA						
80	6E	9E	1C	1C	FC		

To make your own characters, mark a '1' in the segments you want to light up then turn the value into HEXADECIMAL

	'a'	'b'	'c'	'd'	'e'	'f'	'g'	dp
Letter H = 6E	0	1	1	0	1	1	1	0